Qur Docket No.: 42P12483

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re Application of:	
Jamil) Examiner: Lane, John A.
Application No.: 10/039,060) Art Group: 2188
Filed: January 2, 2002)
For: Transfer of Cache Lines On-Chip Between Processing Cores in a Multi- Core System)) _)
PRELIMINARY AMENDMENT	
Mail Stop: RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
Sir:	
Please amend the claims and conside	r the following remarks.
FIRST CLASS CERTI	FICATE OF MAILING
	dence to be deposited with the United States Postal Service as first I that this paper or fee has been addressed to the Commissioner for
	er 28, 2005 Deposit
	chwenke iling Correspondence
Alah Shwenke	
Signature	9/28/05 Date

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